

I claim:

1. A method for supplying a clock signal to processor-controlled apparatuses, which comprises:

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basing a clock frequency fed to a device for determining a clock time and a clock frequency fed to a processor device at times of no processor load or low processor load, on a quartz frequency of the same quartz; and

clocking the processor device with a system clock in third times.

2. The method according to claim 1, which further comprises:

feeding a first clock frequency based on a first quartz frequency or a frequency derived therefrom, to the device for determining a clock time;

selecting the clock frequency to be fed to the processor device as a function of the processor load; and

feeding the clock frequency based on the first quartz frequency or on a frequency derived therefrom, to the

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processor device in first times of no processor load or low processor load.

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3. The method according to claim 1, which further comprises generating the quartz frequency with a clock quartz.

4. The method according to claim 1, which further comprises switching the processor device to a clockless state in second times of no processor load.

5. The method according to claim 1, which further comprises clocking the processor device with a reduced frequency in fourth times of low processor load, the reduced frequency being lower than the frequency of the system clock and higher than the quartz frequency or the frequency derived therefrom.

6. The method according to claim 1, which further comprises initiating, with the processor device, a selection of a clock frequency to be fed to the processor device, being lower than a current frequency fed to the processor device.

7. The method according to claim 1, which further comprises initiating, with the processor device, a selection of a clock

frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

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8. The method according to claim 1, which further comprises initiating, with external events, a selection of a clock frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

9. The method according to claim 1, which further comprises initiating, after expiration of a predefined time period, a selection of a clock frequency to be fed to the processor device, being higher than a current clock frequency fed to the processor device.

10. The method according to claim 1, which further comprises temporarily switching off not-required components of an apparatus as a function of the clock frequency fed to the processor device.

11. In a configuration for supplying a clock signal to processor-controlled apparatuses having a processor device and associated with a device for determining a clock time, the improvement comprising:

a clock selector unit connected to the processor device for selecting a frequency to be fed to the processor device, as a function of a processor load;

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a quartz for generating a quartz frequency and for feeding a clock frequency based on the quartz frequency or a frequency derived therefrom to the device for determining the clock time;

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said clock selector unit feeding a clock frequency based on the quartz frequency or on a frequency derived therefrom to the processor device in first times of no processor load or low processor load, and

the processor device clocked with a system clock in third times.

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